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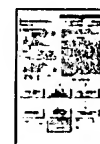
Title: **TW0448407B: HARDWARE ACCELERATOR FOR AN OBJECT-ORIENTED PROGRAMMING LANGUAGE**

Country: **TW Taiwan**

Kind: **B Patent**

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Application Number: **TW1999088120193**

IPC Code: **G06F 9/45; G06F 17/00;**

ECLA Code: **None**

Priority Number: **1999-11-18 TW1999088120193**

Abstract: A method and apparatus for accelerating an object-oriented programming language are provided at a hardware gate level. In a Java-compliant embodiment, a Java Application framework is implemented in hardware. The Java. AWT, Java. NET, and Java. IO application frameworks are supported in the preferred embodiment of the invention. Application framework classes are stored as libraries in a shared memory. Instances and methods of supported application framework classes that are executed by a Java program are offloaded to a hardware object management system. A software stub is provided as an interface between the hardware object management system and the central processing unit. Additional application frameworks can be supported by modifying or replacing the software stub. Hardware object management system requests are executed by an application framework-specific hardware accelerator. Application framework classes are retrieved from the shared memory as needed, and executed instructions are stored to the shared memory to be accessed by the central processing unit. Central processing unit processing of non-supported application framework instructions is continued during hardware accelerator execution of hardware object management system requests.

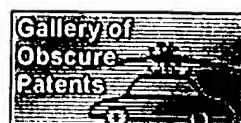
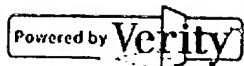
Family:

PDF	Publication	Pub. Date	Filed	Title
<input checked="" type="checkbox"/>	<b>TW0448407B</b>	<b>2001-08-01</b>	<b>1999-11-18</b>	<b>HARDWARE ACCELERATOR FOR AN OBJECT-ORIENTED PROGRAMMING LANGUAGE</b>

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